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Response After BPAI Decision

1. This is in response to a BPAI Decision in which after consideration of the record before us, that the examiner's obviousness-type double patenting rejection is proper. The BPAI Decision also of the view that the disclosure of Lynch does not fully meet the invention as set forth in claims 1-7.

Terminal Disclaimer

2. Subsequently to the BPAI Decision a terminal disclaimer filed on December 20th, 2004 disclaiming the terminal portion of any patent granted on this application which would extend beyond the expiration date of US Patent No. 6,018,756 has been reviewed and is accepted. The terminal disclaimer has been recorded.

Allowable Subject Matter

- 3. Claims 1-7 are allowed.
- 4. The following is an examiner's statement of reasons for allowance:
 - a. Applicant's inventive concept relates to an adder/subtractor contained in a
 floating point unit of a microprocessor. Specifically the invention relates to normalization
 of the results of addition and subtraction operations of floating point numbers.
 Applicant's inventive concept is novel and innovative in the sense is performs an addition

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or the mantissa signals are offset (shifted) by one position when they are to be subtracted from the position they would have if they are going to be added. Additionally, the concept requires that the mantissas be treated differently for an addition operation than they would be treated for a subtraction operation. The closest prior art, Lynch, shifts one of the operand mantissas with respect to the other operand mantissa to effect an exponent alignment of the mantissas before an addition or subtraction is performed. The amount of shifting performed by Lynch is exactly the same whether the operation to be a subtraction. Furthermore, according the BPAI, Lynch discloses treating such mantissas the same regardless of the operation and therefore would not sustain that Lynch discloses the same inventive concept as the claimed invention.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Firmin Backer whose telephone number is (571) 272-6703. The examiner can normally be reached on Mon-Thu 9:00 AM - 5:00 PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, James Trammell can be reached on (571) 272-6712. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Firmin Backer
Primary Examiner
Art Unit 3621

July 21, 2005